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INFORMATION DISCLOSURE CITATION	Docket Number (Optional) PO9-96-134	Application Number
(Use several sheets if necessary)	Applicant(s) **Balaram Sinharoy**	
	Filing Date	Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DJH	A	5,669,001	09/16/1997	Moreno	395	706	03/23/1995
DJH	B	~~5,669,536~~ 5,699,536	12/16/1997	Hopkins et al.	395	392	4/13/1995

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

DJH		E. Rotenberg, S. Bennett and J. Smith, "Trace Cache: a Low Latency Approach to High Bandwidth ~~Fetching~~, Instruction Fetching April 11, 1996, pp. 1-48
DJH		T. M. Conte, K. N. Menezes, P. M. Mills and B. A. Patel, "Optimization of Instruction Fetch Mechanisms for High Issue Rates," in Proceedings of the 22nd Annual International Symposium on Computer Architecture, (Santa Margherita, Italy), Jun. 1995, pp. 333-344

EXAMINER David J- Athyman	DATE CONSIDERED 3-2-04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

	INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)*	Docket Number (Optional) **PO9-96-134**	Application Number
		Applicant(s) **Balaram Sinharoy**	
		Filing Date	Group Art Unit

*EXAMINER INITIAL		OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*
𝒟𝒥𝐻		Tse-Yu Yeh, D. Marr and Y. Patt, "Increasing the Instruction Fetch Rate Via Multiple Branch Prediction and a branch Address Cache," Procedings of the 7th ACM International Conference on Supercomputing, July 1993, pp. 67-76
𝒟𝒥𝐻		J. E. Smith, "A Study of Branch Prediction Strategies', in 8th Annual International Symposium of Computer Architecture, ACM, 1981, pp. 202-215
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EXAMINER *David J. Khuri*	DATE CONSIDERED 3-2-04

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